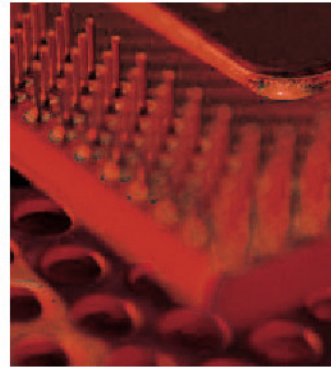




Global Semiconductor Alliance



MS/RF PDK Working Group Meeting

January 31st, 2011



Agenda

- **Welcome**
- **IPL Update**
 - Presented by Jingwen Yuan, Synopsys
- **OpenPDK Update**
 - Presented by Sumit DasGupta, Silicon Integration Initiative (Si2)
- **PDK Survey Results**
 - Presented by Sanjay Krishnan
- **Wrap-Up**





GSA MS/RF PDK WG's Position

1. Having open de-facto standards that create efficiencies in the marketplace are important for the semiconductor industry.
2. Don't want to see the industry spin its wheels to create yet another standard among many.
3. Everyone needs to come together. Must be **driven by the foundries with** support from the EDA vendors.
4. Good planning is necessary - this is not a fast process.

Working Group Purpose:

Creating a industry forum to gain a better understanding of the different formats that currently exist within the industry, how they differ from one another, what they define, who can use them, etc.

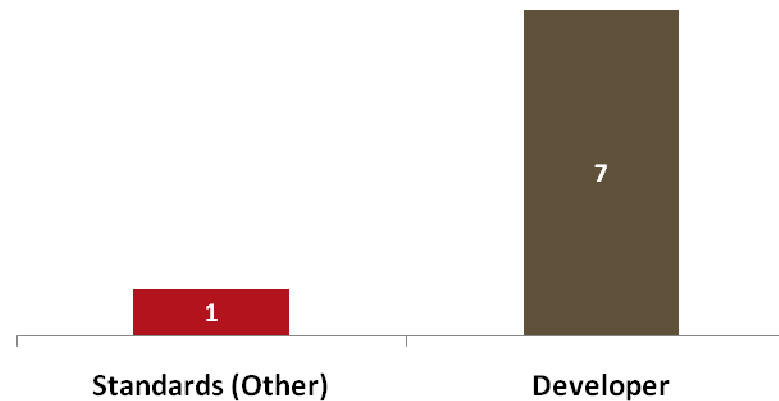




PDK Survey Results

The PDK Survey was sent to members of the PDK working group on 1/17/2011 and closed on 1/28/2011 – the results will set the working group's agenda for 2011.

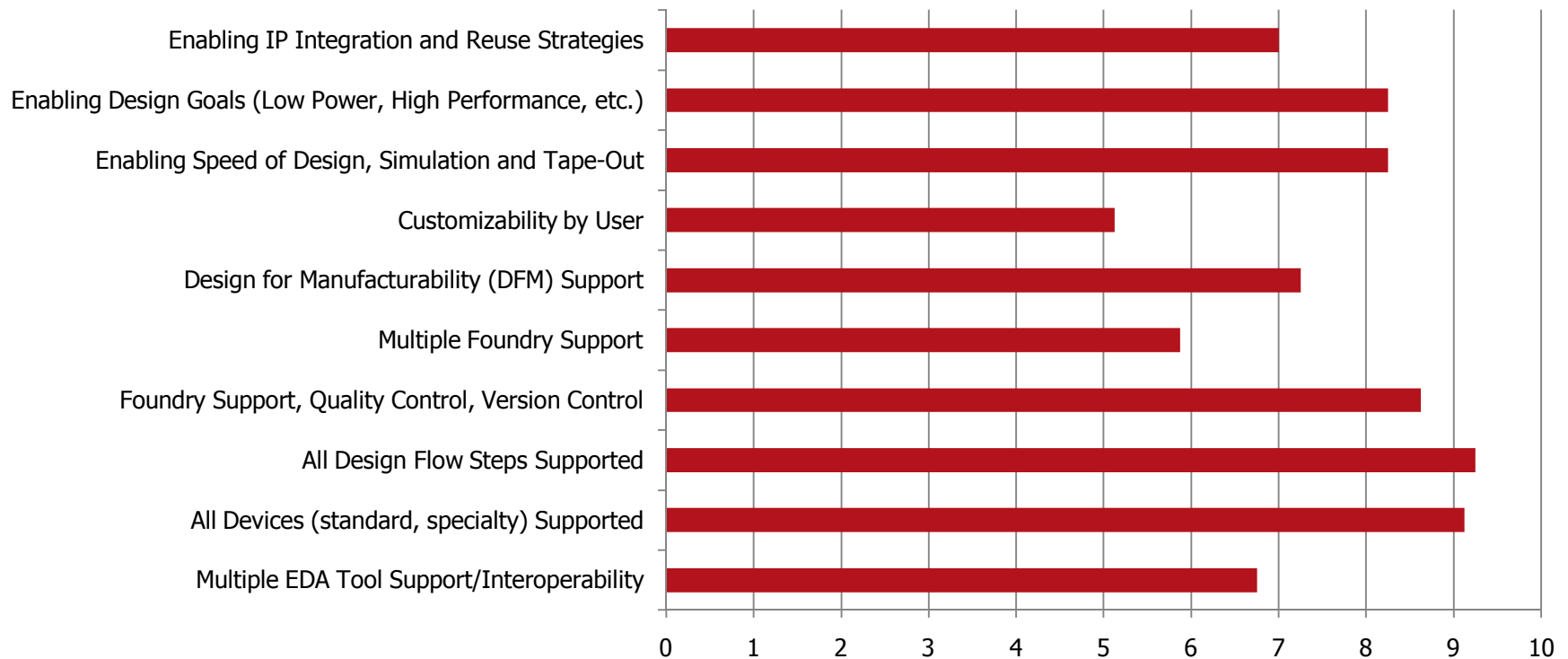
Number of respondents: 8





PDK Survey Results

How would you rate the importance of the following PDK features for you/your organization? (1=Least Important, 10=Most Important)



How would you rate the importance of the following PDK-related areas for you/your organization? 1=Least Important, 10=Most Important

