

MIXED-SIGNAL/RF PDK SUB-WORKING GROUP MEETING

Date: January 26, 2010

Location: Teleconference Only

Time: 10:00am (Pacific)

Attendees:

- Thomas Moerth - austriamicrosystems AG, PDK WG Chairman
- Chelsea Boone, PDK WG GSA Contact
- Paul Double - EDA Solutions Limited
- Richard Morse – SpringSoft
- Joerg Doblaski – X-FAB
- Bernd Fischer-Krellenberg – LFoundry
- Michael Ma - CiraNova, Inc.
- Sumit DasGupta - Silicon Integration Initiative, Inc. (Si2)
- Herb Reiter - eda 2 asic Consulting, Inc
- Tom Quan – TSMC
- Pankaj Mayor – Cadence
- Kasim Mahmood – Qualcomm
- John Laughlin - Dongbu HiTek Co., Ltd.
- Xingang Wang - Skyworks Solutions, Inc.
- Kevin Kranen – Synopsys
- Wan-Fu Chen – Inphi

Minutes:

iPDK:

- TSMC is currently working on a narrower definition of a PDK: they are using the symbols from Si2, pcells in python with PyCell Studio, callbacks in TCL, iCDF from Synopsys.
- The first iPDK was for 65nm LP + RF components.
- TSMC is working with IPL to get everything settled.
- iDRC and iLVS are not finalized yet. TSMC works with all major EDA vendors to finalize the iDRC/iLVS standard.
- TSMC will most likely contribute iDRC/iLVS to a standards organization. Si2 is a potential candidate.
- A Cadence Skill PDK is part of the iPDK to make it work with Cadence Software.
- TSMC will roll out iPDK for many (larger and smaller) nodes in the future.

Si2:

- The OpenPDK project was approved by the Si2 board
- The Si2 efforts will standardize the following:
 - symbols
 - pcells
 - callbacks
 - CDF
 - standard socket for SPICE engines
 - additions to the OA techfile
 - standard for DRC and LVS run sets

Some bullets from the discussion:

- Cadence does not qualify Virtuoso with PyCells

- The design house expectation is that the foundries qualify a (i)PDK for various tools

Next Meeting for PDK Sub-Working Group:

Date: March 23, 2010
Time: 10:00am (Pacific)
Place: Teleconference
Agenda: TBD