

## **MIXED-SIGNAL/RF PDK SUB-WORKING GROUP MEETING**

**Date:** March 30, 2010

**Location:** Teleconference Only

**Time:** 10:00am (Pacific)

### **Attendees:**

- Thomas Moerth – austriamicrosystems AG, PDK WG Chairman
- Chelsea Boone, PDK WG GSA Contact
- Herb Reiter – eda 2 asic Consulting, Inc
- Tom Quan – TSMC
- Pankaj Mayor – Cadence
- Sabine Perko – X-FAB Semiconductor Foundries AG
- Christine Dufour – Altis Semiconductor
- Michael Buehler-Garcia – Mentor Graphics
- Wan-Fu Chen – Inphi
- Pekka Ojala – Exar

### **Sub-Working Group Purpose:**

Creating a industry forum to gain a better understanding of the different formats that currently exist within the industry, how they differ from one another, what they define, who can use them, etc.

### **Minutes:**

Status from Mentor:

- Works with TSMC on iDRC and iLVS definition.
- iDRC defines the process rules on a higher level, and each EDA vendor can translate these runsets in native language to run in own tool.
- TSMC and Mentor currently working out the legal documents to donate iDRC to Si2.
- Synopsys donated a patent around iDRC to Si2 to make it a free, open standard.
- It also intends to donate iLVS; however, it would first be finished and proven in a production PDK, and then donated.

Status from TSMC:

- IPL Alliance released the iPDK standard at the end of February with a 90nm-generic iPDK available for download at [www.iplnow.com](http://www.iplnow.com).
- This kit is identical to the TSMC iPDK, but process information is generic.
- Generic PDK is validated with Synopsys, Springsoft, Magma, ... tools
- There is an ongoing discussion with Si2 to potentially donate iPDK.

### **Next Meeting for PDK Sub-Working Group:**

Date: May 25, 2010

Time: 10:00am (Pacific)

Place: Teleconference

Agenda: (1) Update from Si2 on OpenPDK

(2) Update on who has used the iPDK standard released by the IPL Alliance and their feedback