

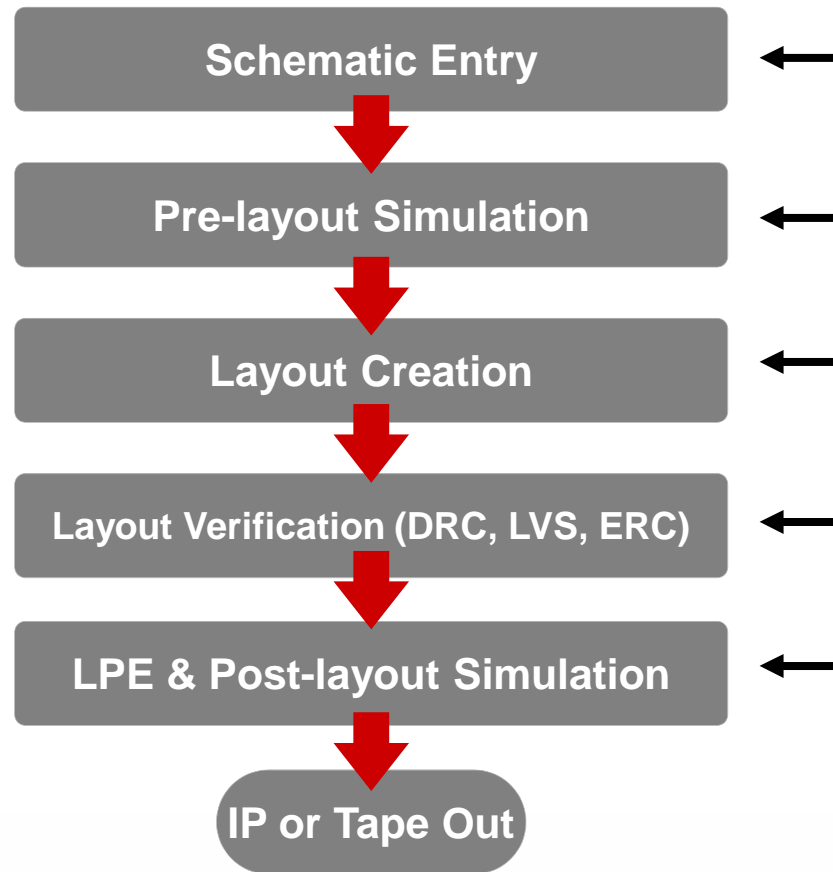
# TSMC iPDK



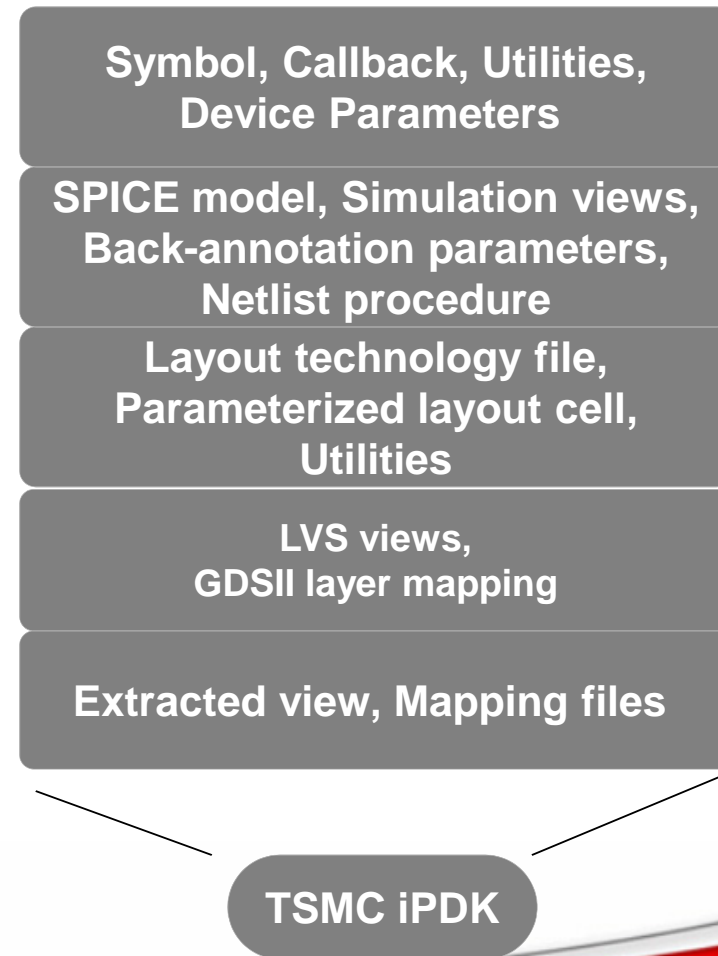
**Tom Quan**  
Design Methodology & Service Marketing  
**TSMC**

# TSMC iPDK

## Analog/RF design flow

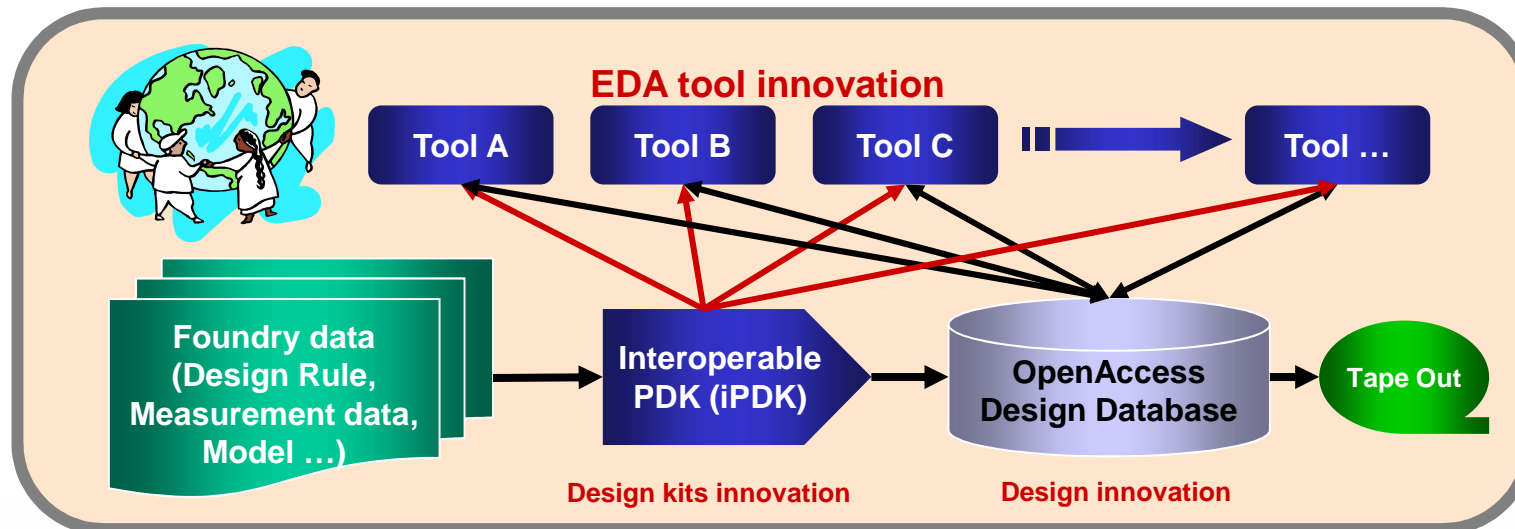


## iPDK

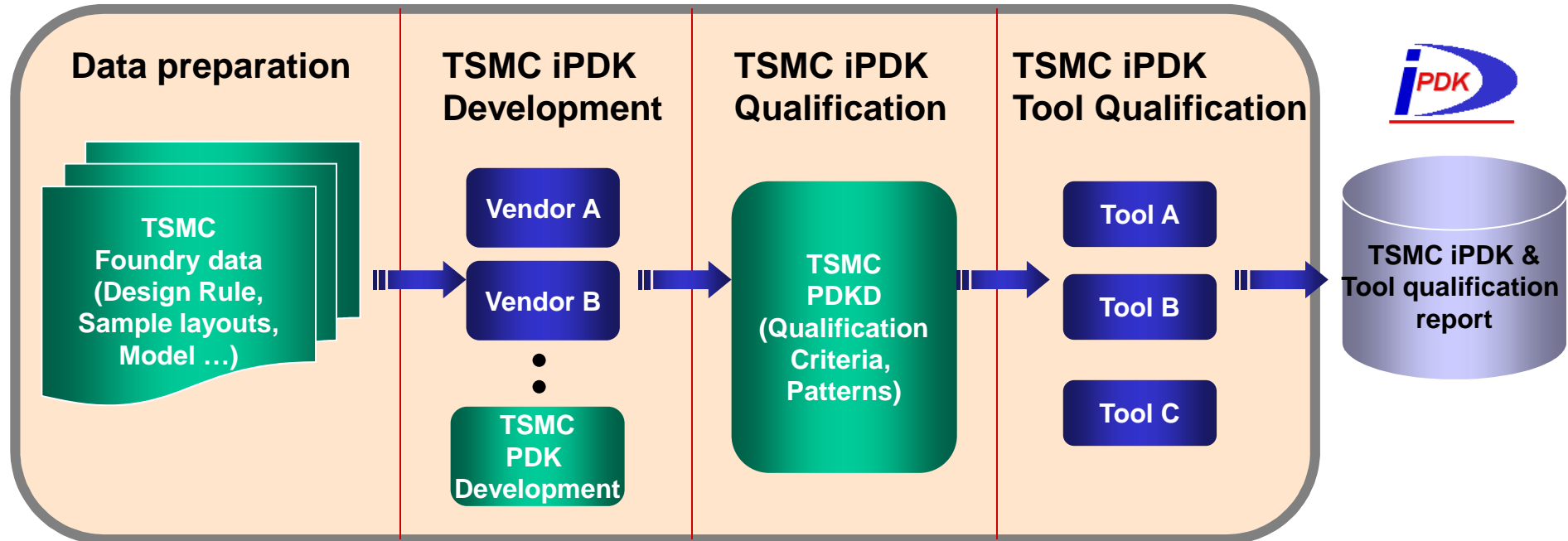


# TSMC iPDK

- Use IPL Alliance 1.0 iPDK as base
- Database – OpenAccess
- Schematic Symbols – OA standard symbol set
- Parameterized cells – Python-based, and SKILL-based
- Callbacks – Tcl and SKILL



# TSMC iPDK Validation Process



# Summary

- **TSMC fully commits to iPDK**
  - **First iPDK (65LP) available in 2009**
  - **Support all nodes from 0.18um to 28nm**
  - **All iPDKs pass rigorous TSMC and multi EDA vendor qualification**