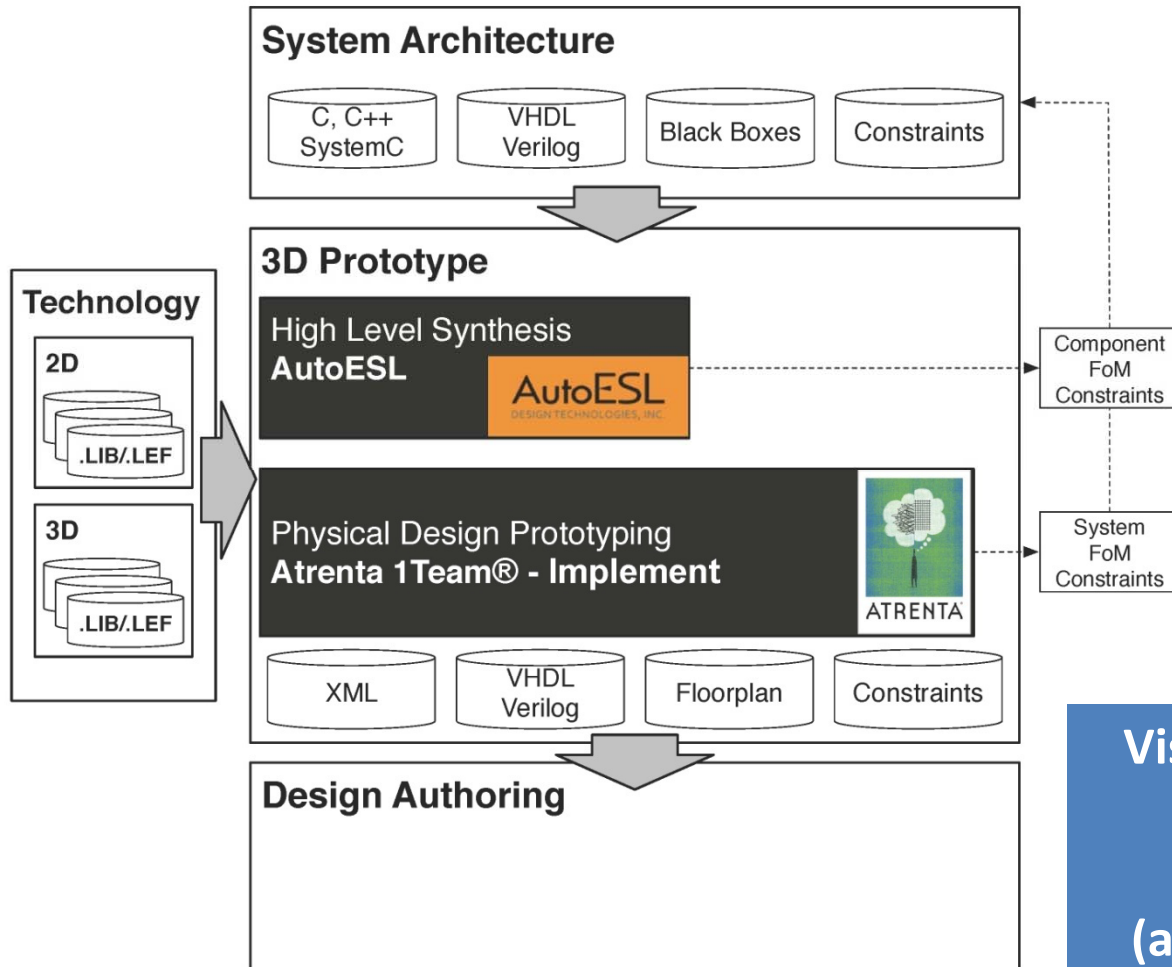


Key 3D design steps (Please don't change this column)	Brief description of your tool 's purpose (replace these simple examples with a few lines of your info)	Remarks (or product names)
Technology aware high-level synthesis	<ul style="list-style-type: none"> Automated generation of various RTL implementations using different technology nodes and synthesis directives 	- See the live demo
Multi-chip planning, partitioning and prototyping	<ul style="list-style-type: none"> Development of physical prototype at RTL for various partitioning and interconnect scenarios – create initial floorplan and quality of results metrics 	- See the live demo

This is a joint demo of a working front-end 3D design flow co-developed by Atrenta, AutoESL and IMEC with participation from Qualcomm



- High-level overview of flow demo

Visit the Atrenta Booth #744 or the AutoESL booth #1577 to sign up for a demo slot (and get your cool T-shirt, too)