

SUPPLY-CHAIN PERFORMANCE WORKING GROUP MINUTES

Date: February 22, 2010
Location: Teleconference Only
Time: 9:30am (Pacific)
Participants:

Chelsea Boone – GSA
Matt Cheatham – PDS
Dan Wark – Amalfi Semiconductor
Ed Pausa – PricewaterhouseCoopers LLP
Jay Seaton – Evolutionary Operations Consulting LLC
L.T. Guttadauro - Fab Owners Association
David Busch – SVTC
Mark Ashton/George Wu – PMC-Sierra
Kurt A. Wolf – Silicon-IP, Inc.
Seyed Attaran – PLX Technology
Kerry Enright – Enright Enterprise Consulting Solutions
Ron Jones – N-Able Group International
Larry Sladewski – NVIDIA Corporation
Marta Garces – ADD Semiconductor

Meeting Purpose:

- To discuss the Q1 2010 Wafer & Back-End Pricing Survey Results.

End Result/Conclusions:

- **Q1 2010 WP & BE Survey Results:**
 - Assembly
 - Delete lines 2-3,13 in spreadsheet.
 - Lines 4-12, 14-15 are okay.
 - PDS Questioning .101 data entry that was not flagged.
 - We will change filters for those that we have kept multiple times.
 - Wafer & Mask
 - Delete lines 2-4,7 (delete mask cost), 10-19, 43 (delete mask cost).
 - Keep lines 5-6, 8-9, 20-42,44-45
 - We will change filters for those that we have kept multiple times.
 - We will now have a volume filter of 25 for “# of wafers purchased last quarter”
 - Add 40/45nm and delete .08 in process geometry field in survey and report
- **Future Timelines/Dates for Next Survey:**
 - Q2 2010 Wafer & Back-End Pricing Survey Released: Beg. Apr 2010
 - Q2 2010 Wafer & Back-End Pricing Survey Ends: End of Apr 2010
 - Q2 2010 Working Group Meeting to discuss Q2'10 results: 2nd week of May 2010