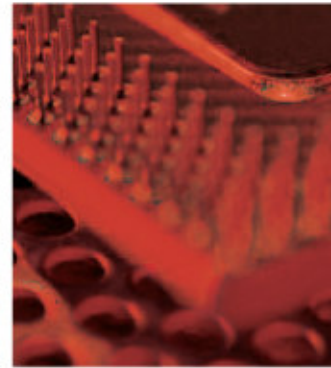




**FSA**



# FSA MIXED-SIGNAL/RF SPICE MODEL CHECKLIST

EDA Vendor Benefits

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## EDA VENDOR BENEFITS SPICE MODEL CHECKLIST

- Easier to support AMS/RF IC design customers
  - Quickly separate problems between circuit, model, simulator, and cockpit errors
  - Minimize version confusion- models, parameter libraries, documentation, software versions used for validation
  - Quickly understand value ranges of models used (features, completeness, statistical robustness)
  - Quickly understand ranges of extraction (test chip devices, environment, measurement limits)
  - Holds qualified simulator vendors accountable
  - Easy to understand discrepancies with model/simulator combination





## EDA VENDOR BENEFITS SPICE MODEL CHECKLIST

- Easier to Support Foundry Modeling Partners
  - Enhances communication with a common language
  - Simulator and model version control
  - Consistent documentation of modeling issues
- Easier to Support Foundry PDK Partners
  - Major problem in PDK development is accurately modeling devices created with Pcells
  - Checklist serves as common language between PDK and Modeling teams





## EDA VENDOR BENEFITS SPICE MODEL CHECKLIST

- Competitive Advantages
  - Showcases the capabilities of superior software tools
    - Performance
    - Accuracy
    - Convergence
    - Ease of use
  - Educates IC designers to understand and select superior software products and services
    - Creates apples-to-apples benchmarks for EDA tools
  - Encourages EDA tool selections based on merit
    - ROI
    - Cost vs. Performance

