

## **MIXED-SIGNAL/RF SUBCOMMITTEE MINUTES PCM/PROCESS CHECKLIST WORKING GROUP**

**Date:** December 4, 2007

**Location:** Conference Call

**Time:** 10:00am (PST)

### **Attendees:**

- Robb Johnson - Inphi Corp.
- Chelsea Boone – GSA
- David Schwan - Sirenza
- Aniruddha Joshi - Skyworks Solutions, Inc.
- Thomas Moerth - austriamicrosystems AG

### **Summary:**

- Discussed version 0.5 of the MS/RF Process Checklist.

### **Discussion/Action Items:**

- Is Flip Chip available? Add question about if this is an in-house capability or if a third party is used?
- Remove Capacitor Leakage
- Get rid of lines describing cap/area on varactors
- Remove diode leakage
- Add fuse to IP section
- Split minimum lot size into two questions: Production Lots and Engineering Lots.

### **Next Meeting for PCM/Process Checklist Working Group:**

- February 20, 2008 at 10:00am (PST) on teleconference.