

MIXED-SIGNAL/RF SUBCOMMITTEE MINUTES PDK CHECKLIST WORKING GROUP

Date: February 26, 2008

Location: Teleconference Only

Time: 10:00AM PDT

Attendees:

- Yunsu Lee – MagnaChip
- David Cunningham – IBM
- Ted Paone – Cadence Design Systems
- Thomas Moerth – Austriamicrosystems
- Chelsea Boone – GSA
- David Schwann – Sirenza
- Sabine Perko – X-Fab
- Fu Dain – Simucad
- Jingwen Yuan – Synopsys

Summary:

- Discussed how the existing PDK checklist could be extended for OpenAccess-based design kits.
- Current PDK and Model Checklist Components are Available at:
<http://www.gsaglobal.org/subcommittees/msrf/index.asp>.

Discussion Items:

- Customers are working on OA. There are tapeouts, but mostly by IDMs.
- For the migration of IP to Open Access, a guideline would be appreciated. Ted Paone will check whether he can supply his white paper on this topic to GSA.
- Should we create a migration checklist or add guidelines to existing checklist?
- How do we ensure the translation is the same as before? Do the layouts, simulations and netlists match?
- New items to add to the checklist: what type of PCells is used, what language are they written in, verification, info on data model that the PDK checklist supports.
- Thomas will create a proposal for the next meeting explaining how the PDK checklist could be extended for Open Access-based design kits.

Next Meeting for PDK Checklist Working Group:

The next meeting is scheduled for April 22, 2008 at 10:00AM PDT.