



Building Europe's
Sovereign CPU Champion:
**Challenges &
Successes**

GSA
2026 ENTREPRENEURSHIP CONFERENCE

APRIL 29, 2026

The vision that led to SiPearl's inception

In 2015, supercomputers and data centres that processed Europe's strategic data ran mostly on non-European hardware. I had an intuition:

// Europe will only gain strategic Independence when it has sovereign high-performance CPUs⁽¹⁾. //



EuroHPC
Joint Undertaking



Our mandate from Europe: Strategic independence in high-end CPUs

A dramatic situation for Europe

Strategic data
are processed by
non-European CPUs

Huge impact on:

- Security
- Safety
- Sovereignty

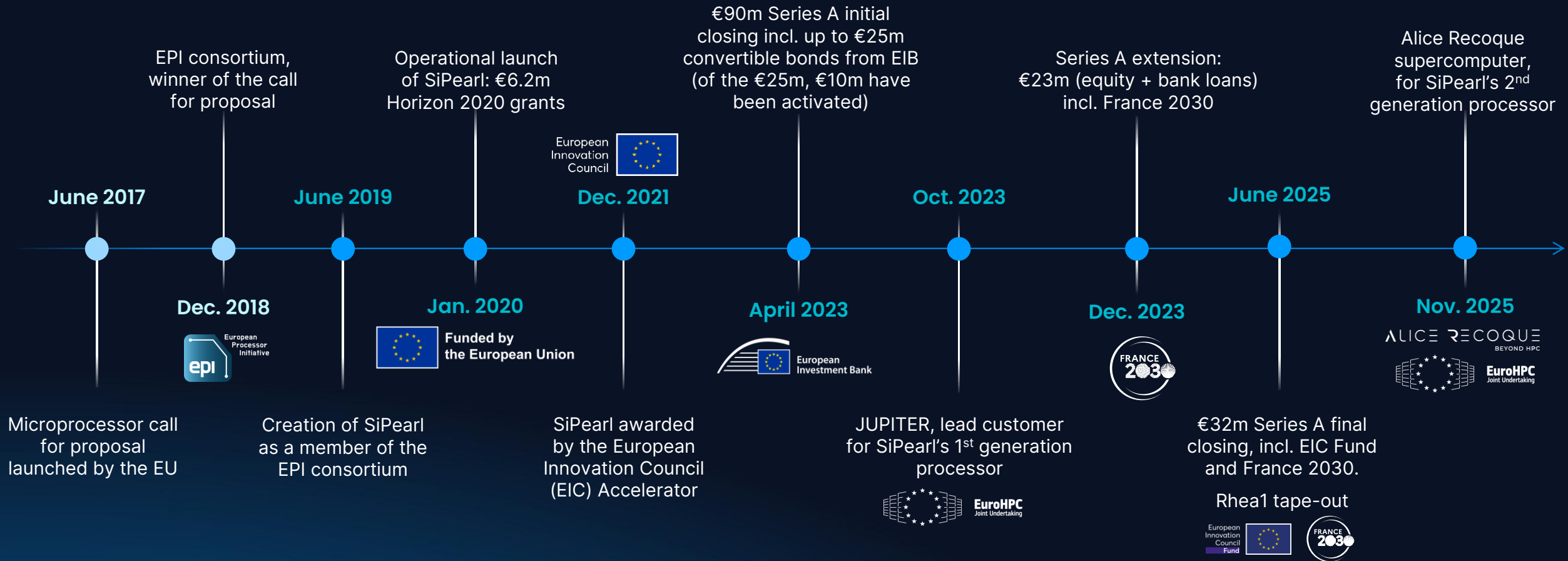


No European
digital sovereignty
without
sovereign hardware

SiPearl's CPUs will fuel Europe's



From a European Union concern to SiPearl's ramp-up



A journey marked by challenges and successes

Risks & challenges

Access

Funding



Markets



Human Resources



"The enemy from within"



Fundraising campaign

A complex process

3 years instead of the planned one year for the first round

1. Covid-19
2. A high-risk sector of activity, little known to European private equity firms

However, we were able to count on

1. The French tax mechanism, the "CIR"
2. France 2030 government plan
3. The European Innovation Council (EIC Accelerator)
4. European collaborative projects, in particular European Processor Initiative (Horizon 2020, then Horizon Europe)
5. France: the support of the CEA, the Elysée Palace, the secretariats and ministries responsible for digital affairs, the MESR and the AID / Armed Forces.

Seed Money
Operational launch

€7.4m
Horizon 2020 grants

January 2020

Initial closing

Up to €90m
incl. up to €25m⁽¹⁾ convertible bonds from EIB

April 2023

Series A: €130m

Extension

€23m
€15m equity + €8m bank loans

December 2023

Final closing

€32m
convertible bonds

June 2025

Launch of Series B round

Since early 2026

A European company

France
4 R&D centres
Maisons-Laffitte (HQ) *June 2019*
Grenoble *Sept. 2021*
Massy *Sept. 2021*
Sophia Antipolis *Sept. 2022*
2 sovereign data centres

Germany
1 R&D centre
Open *May 2020*
Closed *Dec. 2024*

Italy
1 R&D centre
Bologna *May 2024*

Spain
1 R&D centre
Barcelona *June 2021*

Taiwan
1 R&D centre
Hsinchu *July 2026*

The challenges

- Establishing our own sovereign data centres
- Security constraints
- Recruitment screening processes
- Access to engineering talents
- Work-life balance

and this was before the "28th regime / EU Inc" 😊

Built-in sovereignty since the inception

2 Sovereign design & data centres
located in northern France

€20m investment
Sovereignty comes at a price

3 PB

5 Backup Servers

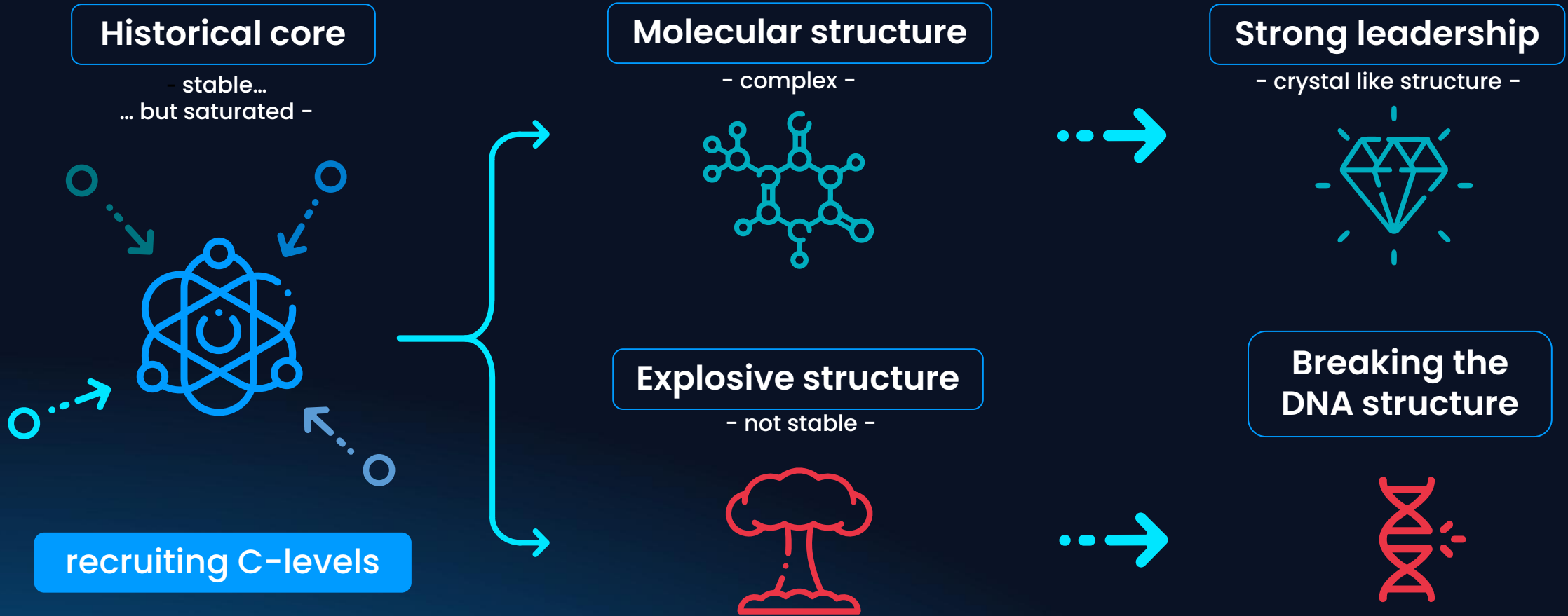
Cohesity & Veritas technologies
Eppes & Reims

Unique EDA design center
in Europe

EMULATOR

C-level recruitment = tickling the dragon's tail?

The risk of weakening a team's DNA



And finally... a world-class "Processor Team"...

Leadership



Philippe Notton
CEO & Founder

Bull STI MEDiatek



Guillaume Etorre
Chief Operating Officer

DEVIALET SIGMA ZORAN



Ying-Chih Yang
Chief Scientist Officer

Bull STI MEDiatek



Delphine Démeron
VP Human Resources

DIGITAL REALTY IBM agorapulse



Gaël Paul
Senior VP Engineering

Accelize PLDA



Jean-Luc Gilbert
Group CFO & IR

Alcedia BRITVIČ STI



Vincent Casillas
Chief Technology Officer

Valeo STI



Craig Prunty
VP Marketing & Business Dev.

MARVELL CAVIUM



Anna Riverola
International Dev. & Public Affairs Director
Country Manager - Spain

RSB
Barcelona Supercomputing Center
CONSEJO REGULADOR DE SUPERCOMPUTACIÓN

First product: RHEA1 for HPC and AI

The most complex CPU ever designed in Europe (*under production*)

SILICON PROCESS **6** nm TSMC N6

LOGIC DIE IN ADVANCED SILICON PROCESS

80 arm® Neoverse V1 cores⁽¹⁾

+61 billion transistors

7.8 billion equ. gates

16 metal layers

6 PCIe x16 Gen5

2 PCIe x4 Gen5

4 DDR5 interfaces

80 MB distributed SLC

Commercial launch: H2 2026

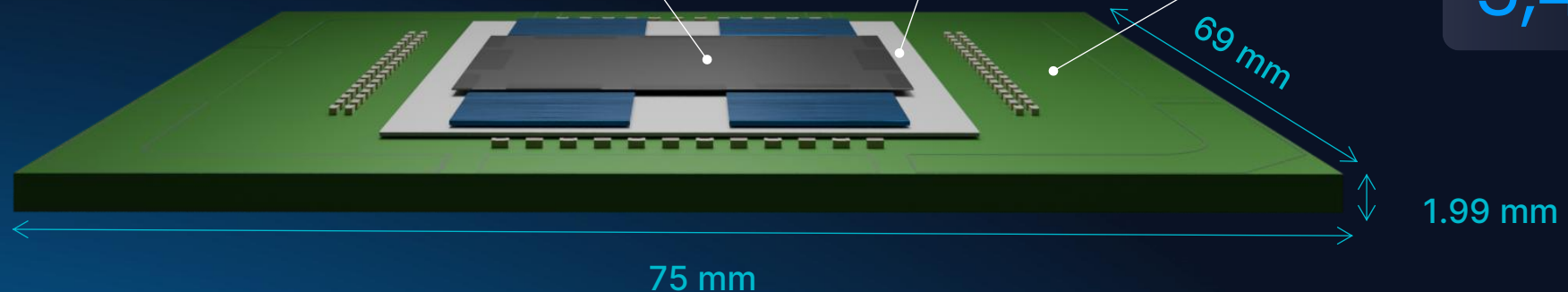
SILICON INTERPOSER

2.5D advanced packaging

4 HBM 4x16=64GB

PACKAGE

5,430 pins



SiPearl in a nutshell



Incorporated

In France in June 2019,
deployed in Spain, Italy



Seed funded

By the European Union
& France



Financing

Series A: €130m



Sovereign design

Data centre in France

258 servers / 212TB memory
14,000 cores / 3PB storage



Technological partnership

Intellectual property

arm



200 employees

from Atos, MEDIATEK, ST, MARVELL,
NXP, HPE, intel, NOKIA



1st tape-out done

June 2025



Fabless IC design house

Manufacturing entrusted to TSMC with N6P



2 design wins

JUPITER & Alice Recoque, 2 first
European exascale supercomputers

sipearl.com

Philippe Notton
CEO & Founder
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