# GSA

# 2024 TECH SUMMIT

SAMSUNG

SAMSUNG

# Imagination brought to life

**Never stop imagining.**
**Our tiny semiconductors give life**
**to your imagination.**

We aspire to enhance quality of life
and create a sustainable future.

We focus on developing innovative technologies
and solutions through close collaboration.
with customers and partners, fostering a culture
of innovation ecosystem growth.

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# ABOUT
# GSA

**GSA is Where Leaders Meet to establish a profitable and sustainable semiconductor ecosystem. This expanding ecosystem encompasses semiconductors, software, solutions, systems and services. As a leading semiconductor and technology industry organization, we offer an efficient and strategic platform for thought leadership.**

**GSA has an impressive global footprint representing 300+ corporate members on six continents. As a result of our unique, neutral platform, our membership ranges from the most exciting, emerging companies to semiconductor industry stalwarts and technology leaders. Our members now represent 75% of the $550B plus semiconductor industry.**

# INTEGRITY · COMMITMENT · INNOVATION · CUSTOMER TRUST

## TSMC Core Values

### Integrity
Integrity is our most basic and most important core value. We tell the truth. We do not make commitments lightly. Once we make a commitment, we devote ourselves completely to meeting that commitment.

### Commitment
TSMC is committed to the welfare of customers, suppliers, employees, shareholders, and society.

### Innovation
Innovation is the wellspring of TSMC's growth, and is a part of all aspects of our business, from strategic planning, marketing and management, to technology and manufacturing.

### Customer Trust
At TSMC, customers come first. Their success is our success, and we value their ability to compete as we value our own. We strive to build deep and enduring relationships with our customers, who trust and rely on us to be part of their success over the long term.

# PROGRAM

| | |
|---|---|
| 9:00 AM – 9:20 AM | **Opening Remarks** |
| 9:20 AM – 9:40 AM | **Semiconductor Industry - Current Trends and Future Opportunities**<br>Gaurav Gupta / Vice President Analyst, Emerging Trends and Technologies / Gartner |
| 9:40 AM – 10:40 AM | **Technologies Beyond Moore**<br><br>**Semiconductor Material Innovations: Shaping the AI-Driven Future of the Semiconductor Industry**<br>Tomomitsu Maoka / Chief Strategy Officer & Chief Risk Management Officer / Resonac<br><br>**Extending Moore's Law: Chiplets and Heterogenous Integration**<br>Maulik Shah / Director of Marketing and Business Development / Intel<br><br>**The Impending Wave of Silicon Quantum Computing**<br>Jason Lynch / CEO / Equal1 |
| 10:40 AM – 11:10 AM | **Networking Break** |
| 11:10 AM – 12:30 PM | **Confluence of Emerging Trends**<br><br>**Learning from the Frontline: Shaping the Future with Generative AI and Path to Adoption**<br>Padampreet Singh / Managing Director Strategy & Consulting Data & AI Practice / Accenture<br><br>**Organizing All Information (with AI)**<br>Cliff Young / TPU and Anton Architect; MLPref co-founder / Google<br><br>**Panel Discussion**<br>Moderator<br>• Chowdary Yanamadala / Senior Director, Technical Strategy / Arm<br><br>Panelists<br>• Cliff Young / TPU and Anton Architect; MLPerf co-founder / Google<br>• Padampreet Singh / Managing Director Strategy & Consulting Data & AI Practice / Accenture<br>• Pradeep Dubey / Senior Fellow / Intel<br>• Subramanian Kartik / Global Vice President / VAST Data |

# PROGRAM

| | |
|---|---|
| 12:30 PM – 1:30 PM | **Networking Lunch** |
| 1:30 PM – 2:45 PM | **Interest Group Breakout Sessions** |

- Emerging Technologies Interest Group
- Intellectual Property Interest Group
- Sustainability Interest Group

| | |
|---|---|
| 2:45 PM – 3:15 PM | **Networking Break** |
| 3:15 PM – 4:35 PM | **Navigating Industry Hurdles** |

**Cybersecurity for the Semiconductor Industry**
Upen Sachdev / Principle Partner / Deloitte

**Semiconductor Manufacturing - Fab Investment & Capacity Trends**
Annie Rockroth / Senior Vice President / ATREG

**Talent Management Fireside Chat - Beyond Moore's Law: Solving the People Problem in Semiconductors**
Moderator
- Lisa Ryan / Partner / Fragomen

Panelists
- Suzanne Plummer / Fellow, Corporate Vice President, Technical Diversity and Development / AMD
- Marc Canel / Vice President, Business Development / Ventana Micro Systems

| | |
|---|---|
| 4:35 PM – 4:40 PM | **Closing Remarks** |
| 4:40 PM – 6:00 PM | **Networking Reception** |

# Allan Cox

**Advisor**
**Silicon Catalyst**

Allan is currently an advisor to several startup companies in the US and EU with a common interest in applying advances in semiconductor technologies to global markets.

He started his career in MOS process development and first product implementations and was a member of teams that developed the first EU single chip microprocessors and single board computers. Allan founded the ASIC Business and Technology Office at Toshiba and the CTO Office for LG in the US. He was a member of several boards linked to both. Allan has successfully managed startup companies as a COO and as a CEO and co-founder, targeting semiconductor product solutions.

Allan attended the University of Bath, UK and has a B.Sc. in Applied Physics. He is an avid soccer player and fan of the beautiful game, but still struggles to get his golf handicap below 14!

# Annie Rockroth

**Senior Vice President**
**ATREG**

Annie joined the ATREG team in January 2016. As Senior Vice President, she is responsible for advising the world's most reputable chip makers on their global strategic manufacturing asset sales and acquisitions. Over the past seven years, Annie has successfully led both sell- and buy-side semiconductor fab and cleanroom transactions for such clients as Elmos, GlobalFoundries, Nexperia, Micron, onsemi, Renesas, TDK, Texas Instruments, and VIS amongst many others. In 2023, she assisted Nexperia with the sale of its operational 200mm Newport UK fab to Vishay Intertechnology.

In 2022, she facilitated the disposition of onsemi's operational GaN-capable 150mm fab in Oudenaarde, Belgium to Belgian photonics company BelGaN Group B.V. In 2019, Annie was instrumental in Wolfspeed's decision to select the Marcy Nanocenter in NY's Mohawk Valley to build the world's first, largest, and only 200mm greenfield silicon carbide (SiC) manufacturing facility. In 2017, she led the sale of Texas Instruments' operational 200mm / 150mm fab in Greenock, Scotland, UK to Diodes.

Before joining ATREG, Annie served for three years as Business Development Manager for international recruitment firm SThree in San Francisco, Calif. In 2013, she won the company's annual Tech Industry Top Biller Award for SThree America for her outstanding revenue-generating performance.

Annie holds a BA degree in both Political Science and Environmental Studies from Santa Clara University in Santa Clara, Calif., USA and completed the Women's Leadership Development Program from the Oxford Saïd Business School, UK. She is a regular sought-after speaker at global semiconductor industry conferences as well as the published author of articles on topics ranging from the state of the global manufacturing asset market and human capital retention to diversity and inclusion in the semiconductor industry.

# Chowdary Yanamadala

Sr. Director,
Technical Strategy
Arm

Chowdary Yanamadala currently leads security IP and services at Arm (A Soft Bank company). He is responsible for advancing Arm's security solutions in the market and identifying new business opportunities in the security space. Before Arm, he served as CTO and SVP of ChaoLogix. Chaologix was acquired by Arm in 2017. Before Chaologix, Mr. Yanamadala served as a Business Director at Maxim Integrated, and he was responsible for leading and advancing the Embedded Security business unit. He was responsible for leading the design and market introduction of several successful product lines.

Mr. Yanamadala also serves as an advisor for three startups in the areas of IoT, data analytics and clean energy. He holds an MBA from Cornell University and is trained in Electrical engineering at Texas A&M University (MSEE) and Osmania University (BSEE). Mr. Yanamadala is a prolific inventor with about 20 patents either granted or in pending status. Besides his professional activities, Mr. Yanamadala is interested in areas of policy-making related to technology and economic development.

He currently serves on the Regional economic development board for the city of Wilmer. In the past, Mr. Yanamadala had the opportunity to work with the White House on technology advancement and international trade policy.

# Cliff Young

TPU and Anton architect;
MLPerf Co-founder
Google

Cliff Young is a software engineer in Google DeepMind, where he works on codesign for deep learning accelerators. He is one of the designers of Google's Tensor Processing Unit (TPU) and one of the founders of the MLPerf benchmark. Previously, Cliff built special-purpose supercomputers for molecular dynamics at D. E. Shaw Research and was a Member of Technical Staff at Bell Labs.

Cliff holds AB, MS, and PhD degrees in computer science from Harvard University. Cliff is a member of ACM and IEEE.

# Frank Schirrmeister

Vice President, Solutions & Business Development
Arteris

Frank Schirrmeister is vice president of solutions and business development at Arteris. He leads Arteris activities in the industry verticals, including automotive and technology horizontals, such as artificial intelligence, machine learning, and safety.

Before Arteris, Frank held senior leadership positions at Cadence Design Systems, Synopsys, and Imperas, focusing on product marketing and management, solutions, strategic ecosystem partner initiatives, and customer engagement.

# Gaurav Gupta

VP Analyst, Emerging Trends and Technologies Gartner

Dr. Gaurav Gupta is a VP Analyst in the Emerging Trends and Technologies team. Dr. Gupta's research areas include semiconductor manufacturing and design trends, semiconductor equipment spending, and chip industry economics and supply chain. Dr. Gupta also covers emerging areas, such as evolving vehicle architecture, autonomous vehicles, Quantum Computing, and energy-efficient compute solutions.

Prior to joining Gartner, Dr. Gupta worked as a Knowledge Expert at McKinsey & Co. where he advised clients across the globe in the semiconductor and electronics industry with a focus on business transformation (cost, operations, and growth) and product strategy (launch/development/planning). He has a strong technical background in the semiconductor industry in process, yield, R&D, and integration, having worked at Intel, IBM and GlobalFoundries prior to the stint with McKinsey & Co.

# Jason Lynch

CEO
Equal1

For more than 20 years, Jason has delivered high impact results in executive management. Prior to Equal1, he held several executive management positions at Analog Devices, Hittite and Farren Technology. In these roles, Jason built and managed large teams comprising sales, operation and finance and was responsible for significant revenue targets. He also drove AI for predictive maintenance, made Internet of Things (IoT) a reality, using cutting-edge sensors and Artificial Intelligence (AI) to save factory-owners millions of dollars in unplanned downtime.

Jason received his B.S. in Electrical Engineering and M.S. in Microelectronic Engineering from the National University of Ireland, Cork and an MBA, summa cum laude from the Franklin W. Olin Graduate School of Business at Babson College.

# Jim Elliott

**Executive Vice President, Memory Sales
Samsung**

Jim Elliott serves as Executive Vice President of Memory Sales at Samsung Semiconductor, Inc., where he oversees all sales and marketing activities for Samsung's memory organization in the Americas. Elliott is responsible for a multi-billion dollar annual revenue organization that spans 3D NAND, SSDs, NAND, DRAM, LPDDR, graphics and UFS products and also leads all marketing communications activities for Samsung Semiconductor in the U.S.

Elliott joined Samsung in 2001 and has held leadership positions in both marketing and sales over the past 15 years. A market visionary, he has helped champion the company's memory transition and market evolution to provide a synergistic product portfolio covering the server, data center, PC, tablet, phone, wearable device and automotive markets.

Elliott holds a Bachelor of Arts degree from the University of California at Davis and received a Master's degree in Business Administration from Cal Poly University in San Luis Obispo, CA. An avid wakeboarder and popular speaker, Jim resides in Los Gatos, CA with his wife and two daughters.

# Kaushal Vora

**Senior Director, Business Acceleration and Global Ecosystem**
**Renesas Electronics**

With over 15 years of experience in the semiconductor industry, Kaushal has worked in several technology areas including healthcare, telecom infrastructure, and solid-state lighting. At Renesas, he leads a global team responsible for defining and developing IoT solutions for the company's microcontroller and microprocessor product lines with a focus on AI and ML, Cybersecurity, Functional Safety, and connectivity among other areas.

Kaushal has a Master of Science degree in Electrical Engineering from the University of Southern California.

GSA

## Lisa Ryan
Partner
Fragomen

Lisa is a leader in Fragomen's West Coast global operations. With almost 30 years of experience in immigration law, she understands the bigger picture and broader business objectives of her clients. Lisa provides thoughtful, strategic counsel to companies of all sizes, ranging from those in the early stages of development to large multinational corporations. She helps them centralize their worldwide programs, ensuring they are strategic, cost-effective, compliant, and competitive. Lisa represents corporate clients in various industries and develops programs that work for both the business and its employees.

She is an active member of the American Bar Association's International Law Section and speaks English and Italian. Lisa has an A.B. degree from Cornell University and a J.D. degree from Tulane University School of Law.
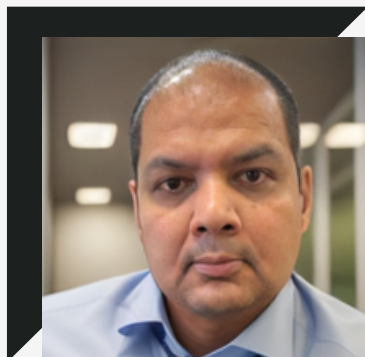
# Marc Canel

Vice President,
Business Development
Ventana Micro Systems

Marc Canel has extensive experience in the processor industry, driving ecosystems and business development for more than 30 years. He is Vice President of Business Development at Ventana Micro Systems, driving the integration of Ventana's world class RISC-V products across markets.

Prior to Ventana, he was at Imagination Technologies where he was Vice President of Business Development promoting graphics and compute products. Prior to Imagination, he was at Arm where he was Vice President of Security Technologies, driving the rollout of security across the Arm ecosystem. Prior to Arm, he was at Qualcomm, driving the rollout of content protection and privacy management technologies. He also worked on the software ecosystem of Qualcomm, supporting the OEMs customers looking for complete solutions. Prior to Qualcomm, he was at IBM where he had various technical and product management roles in the development of data networking products.

# Maulik Shah

Director of Marketing and
Business Development
Intel

Maulik Shah is the Director of Segment Marketing at Intel Foundry where he is responsible for leading customer promotions and engagements for HPC, AI, chiplets and Systems Foundry. Before Intel Foundry, he served as a Product Line Manager for Intel's client business delivering multi-billion dollar revenue with a leadership product portfolio and managed the consumer and education product roadmap and strategy. He has spent 24 years at Intel in various senior leadership roles spanning across the globe including platform architecture, software engineering, platform planning and management, business development, strategic marketing, partner initiatives and customer engagement.

Mr. Shah is an alumni of the Stanford Graduate School of Business (LEAD program) and also holds a Masters degree in Engineering (Computer Science) from the University of Massachusetts. He also holds a Bachelors degree in Engineering (Chemical Engineering) from the University of Mumbai.

# Michael Kurniawan

**Business Strategy Manager**
**Accenture**

Mike Kurniawan is a Strategy Manager in the High Tech practice at Accenture. He focuses on Semiconductor segment and advises clients on growth, product, manufacturing, operations, and ecosystem partnership strategy. Mike has 5 years of industry experience from his time at GlobalFoundries and R&D experience from his 7+ years in academia. He received his PhD in Materials Science and Engineering from Carnegie Mellon University.

Within the context of GSA EmTech interest group, Mike has been a core team member since 2022 (previously AI Interest Group). He's been deeply involved with the interest group team members in publishing white papers, panel discussions, and webinars, touching on the topic of emerging technologies and their impact to the Semiconductor industry.

# Padampreet Singh

Managing Director,
Strategy & Consulting Data
& AI Practice
Accenture

Padampreet is a Managing Director in the Strategy and Consulting Data & AI practice based out of San Francisco. He is the Data & AI Global Lead for High Tech industry and has deep expertise in customer and supply chain functions. He brings strong experience in Data & AI strategy, Modern Data Platform, Operating Model, Data Governance and Digital Transformations.

He has partnered with executive teams at organizations across US, Europe and Asia supporting development of overall Data & AI strategies. His broader experience includes leading engagements such as Modern Data Platforms, Customer 360, Sales & Marketing Analytics, Control Towers, Data Science & AI COEs, Manufacturing Quality Analytics, Master Data Management etc.

Padam holds an M.B.A. and Bachelors degree in Electronics and Communications Engineering.

# Paul Karazuba

**Vice President, Marketing**
**Expedera**

Paul is VP of Marketing at Expedera, a Silicon Valley start-up focusing on best-in-class edge inference Artificial Intelligence engine IP. Paul brings more than 25 years of experience in the semiconductor industry in both chips and IP. Prior to Expedera, Paul was VP of Marketing at PLDA, specializing in high-speed interconnect IP, until its acquisition by Rambus. Before PLDA, he was Senior Director of Marketing at Rambus focusing on Cryptography. Paul has also held various positions at Quicklogic, Aptina (Micron), Foveon, and PerkinElmer.

Paul graduated from Manhattan College with a BS in Management and Marketing.

# Pradeep Dubey

**Senior Fellow, Intel Labs**
**Intel**

Pradeep K. Dubey is an Intel Senior Fellow and director of the Parallel Computing Lab, a part of the Intel Labs organization at Intel Corporation. Since 2003, he has led a team of top researchers focused on state-of-the-art research in parallel computing. Dubey and his team are responsible for defining computer architectures that can efficiently handle emerging machine learning/artificial intelligence, traditional HPC applications for data-centric computing environments, and deriving product differentiation opportunities for Intel's CPU and GPU processing platforms.

He holds 36 patents and has published more than 100 peer-reviewed technical papers. In 2012, Dubey was honored with an Intel Achievement Award for breakthroughs in parallel computing research and was honored with Outstanding Electrical and Computer Engineer Award from Purdue University in 2014.

Dubey earned a bachelor's degree in electronics and communication engineering from Birla Institute of Technology, India; a master's degree in electrical engineering from the University of Massachusetts at Amherst; and a Ph.D. in electrical engineering from Purdue University. He was named a Fellow of the Institute of Electrical and Electronics Engineers in 2001 for his contributions to computer architecture supporting multimedia processing.

# Rajesh Sengamedu

Sustainability
GTM Leader, TMT
Capgemini

Rajesh is the Americas Leader for Sustainability Services, responsible for the Tech, Digital, Media, & Telco industries.

In his role, he helps clients be more sustainable by measuring and reducing their carbon footprint, improving circularity, and reducing supply chain risks. He brings together various assets, experiences, and best practices capabilities across Capgemini to change mindsets.

# Sanjiv Agarwal

Vice President,
Global Semiconductor
Industry Leader
Capgemini

Sanjiv has extensive experience in the Tech & Digital industry, driving ecosystems and business growth for more than 25 years. He is Vice President, Global Semiconductor Industry Leader at Capgemini, driving Industry Strategy, Growth, Thought Leadership, Strategic Partnerships, and Key Global Initiatives.

Prior to joining Capgemini, Sanjiv worked as a Semiconductor Industry leader at Wipro, where he partnered with global semiconductor clients on various digital transformation initiatives (customer experience, operations, product) to drive business growth. He has a strong background in the Tech & Digital industry, having worked at IBM, LTIMindtree and Nokia.

# Subramanian Kartik

Global Vice President,
Engineering
VAST Data

Subramanian Kartik has been the Vice President of Systems Engineering at VAST Data since January of 2020, running the global presales organization. He is part of the incredible success of VAST Data which increased almost 25-fold in valuation and revenue in this period. An accomplished technologist and executive in the industry, he has a wide array of experience in Cloud Architectures, AI/Machine Learning/Deep Learning, as well as in the Life Sciences, covering high-performance computing and storage. He is an expert in Generative AI and has worked for 3 years with NVIDIA on GPU Direct Storage, BasePOD and SuperPOD architectures – especially with Large Language Models (LLMs) and other modern technology. He has had a lifelong deep passion for studying complex problems in all spheres spanning both workloads and infrastructure at the vanguard of current day technology.

Prior to his work at VAST Data, he was with EMC (later Dell) for two decades, as both a Distinguished Engineer and global executive running the Converged and Hyperconverged Division go-to-market. He has a Ph.D in Particle Physics with over 75 publications and 3 patents to his credit over the years. He enjoys mathematics, jazz, cooking and travelling with his family in his non-existent spare time.

# Suzanne Plummer

**Fellow and Corporate Vice President, Technical Diversity and Development AMD**

Suzanne Plummer is a Fellow and Corporate Vice President, Technical Diversity and Development at AMD. In this role, she is responsible for developing the technical talent pipeline for female engineers within AMD.

Prior to this role, she was responsible for the design of AMD discrete and data center graphics products. Since joining AMD in 2002, Suzanne has led several design teams behind AMD's most innovative technologies, including the low power x86 microprocessor used in the world's first APU and the x86 microprocessor powering all of AMD's game console wins. She also directed the teams that designed the Zen architecture – which powers AMD's new microprocessors and drove the company's re-entry into the high-performance x86 market.

Previously, Suzanne worked in performance modeling and design engineering at Alchemy Semiconductor, a MIPS-based processor start-up company acquired by AMD in 2002, where she helped develop the AU1000 MIPS microprocessor. Suzanne started her career in applications engineering at Motorola Semiconductor before transitioning to design engineering, where she held various roles across architecture, performance modeling, RTL design and design management.

Suzanne was the Lead for the AMD Austin site for two years and is Executive Sponsor for the AMD Pride organization, supporting AMD's LGBTQ+ community. She has been involved in the AMD Women's Forum and with AMD's Learning and Development Organization, helping to design and lead technical leadership training classes. She also leads the Fellow's Technical Steering Committee, setting the guidelines and making decision for AMD's top-level of technical promotions.

Suzanne has two patents, ten papers, was selected a 2018 "Women Worth Watching" by Diversity Journal and was one of four finalists for the Global Semiconductor Alliance's 2019 Rising Women of Influence Award.

Suzanne has a BSEE from Rice University and has worked in the semiconductor industry for 35 years.

# Tomomitsu Maoka

**Chief Strategy Officer / Chief Risk Management Officer**
**Resonac Holdings Corporation**

Tomomitsu Maoka serves as the Chief Strategy Officer and Chief Risk Management Officer at Resonac Holdings Corporation, formerly known as Showa Denko. In this role, Maoka oversees various responsibilities, encompassing Corporate Strategy, Public Relations, Government Relations, Economic Security, Risk Management, Legal Affairs, and Corporate Governance.

Prior to his tenure at Resonac Holdings/Showa Denko, Maoka held the position of Senior Vice President at Renesas Electronics Corporation. His tenure at Renesas was marked by diverse roles, including Deputy General Manager of the Automotive Solution Business Unit, General Manager of the China Business Unit, and Head of Corporate Planning. His efforts were pivotal in the post-financial crisis turnaround of Renesas, showcasing his significant contributions across various facets of the organization. Before his engagement with Renesas, Maoka was instrumental in leading Post Merger Integration at Lenovo, following its acquisition of NEC's personal computer business via a Joint Venture. Preceding his stint at Lenovo, he oversaw the industrial business in Japan for Infineon Technologies, where he also spearheaded strategy and business development initiatives within the country.

Earlier in his career, Maoka was affiliated with A.T. Kearney as a management consultant, serving a broad spectrum of major Japanese clients across sectors such as telecommunications and automotive.

Maoka is an alumnus of the University of Tokyo, where he earned both his Master of Engineering and Bachelor of Engineering degrees, specializing in Aeronautics and Astronautics.
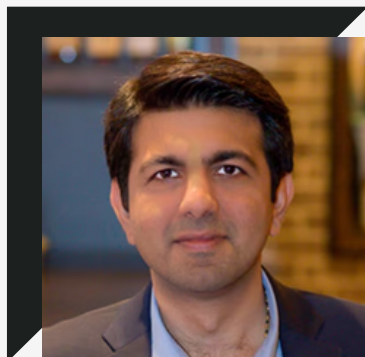
# Tulika Jha

**Director, Logic Design, Azure**
**Microsoft**

Tulika Jha is the Director of Logic Design within Cloud Computing Development Organization at Microsoft. She is SOC Front Design Execution Lead and Manager for current generation of Compute SOC after successfully co-leading front-end design for Cobalt 100, Microsoft's First Datacenter Chip! She is a proven influential leader and manager with 20+ years of industry experience in the Silicon domain. Before Microsoft, Tulika held senior leadership positions as technical lead and manager in design verification, validation, and front-end design both at IP and SOC Level at Intel Corporation. She possesses expertise in silicon development from cradle to grave delivering highly innovative and top performing silicon ranging from IPs and SOCs catering to Client/Desktop/Laptop and Server market.

Tulika also leads various strategic silicon initiatives and standardization efforts within Microsoft across AI & Compute organizations. She is passionate about D&I and empowering people around her. She leads various D&I initiatives like Industry & University Hiring, STEM in local schools and universities, & mentoring engineers across different career stages.

# Upen Sachdev

Principle Partner
Deloitte

Upen leads Deloitte's Google Cloud Cyber Practice and Alliance, driving innovation and expanding advanced cloud security solutions. He is passionately client-focused and brings more than 18 years of extensive technology management experience to his various roles as a Digital Innovation and Strategic Risk executive. An expert in digital transformation and data protection, Upen has spearheaded critical data protection and recovery programs in response to highly publicized breaches across industries.

In addition to his primary role, Upen is the leader of the NextGen CISO Academy, guiding emerging security leaders. He also forges and nurtures strategic client relationships, ensuring tailored solutions and client success leveraging his tenure as the CISO at numerous globally recognized brands.
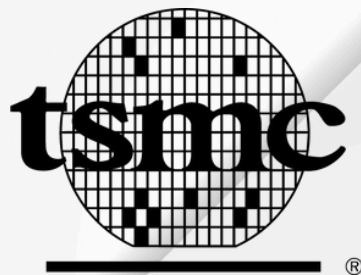
His professional credentials include certifications in Risk and Information Systems Control (CRISC), Information Systems Auditing (CISA), and Information Systems Security (CISSP). Additionally, Upen contributes to academia, industry, and government by developing cybersecurity training and curriculum for senior executives and board members.

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