Event Activities

All Day! Coffee Station sponsored by AsteraLabs

All Day! Wishing Wall sponsored by AyarLabs

All Day! Charging Stations sponsored by CISCO

12:10pm -End of Day Wellness Break sponsored by SAMSUNG

12:10pm -End of Day Photobooth sponsored by CREDO

12:10pm -2:10PM Dessert Bar sponsored by COHERENT

All Day! Wi-fi sponsored by Lam RESEARCH

SSID: LamWISH
Pass: Together2026

Event Agenda

8:00am **Registration & Networking Breakfast**

Sponsored by SIEMENS

9:00am **Welcome Remarks – Annelise McCarthy**

9:15am **Opening Invited Keynote**

Engineered to Thrive: Leading with Integrity
—Powered by Curiosity, Courage & Conviction

Thy Tran / VP of Global Front End Procurement / Micron

10:00am **Panel**

Women Leading the Semiconductor Revolution:
Redefining Leadership for What the Next Decade Demands

Moderator:

- **Audrey Charles** / SVP Corporate Strategy, Advanced Packaging, Lam Research and President, Lam Capital / Lam Research

Panelists:

- **Jennifer Lloyd** / President & CEO / Power Integrations
- **Jutta Meier** / CEO / IQE
- **Rani Borkar** / President, Azure Hardware Systems and Infrastructure / Microsoft
- **Rose Castanares** / President of TSMC Arizona / TSMC

10:45am **Smoothie Break & Transition to Breakout Sessions**

Sponsored by cadence®

Sponsored by power integrations

11:10am

Breakout Sessions

Artificial Intelligence (AI) in Hardware

- **Accelerating FPGA Physical Design Closure through Generative AI**
  - Mrunal Sagare / Physical Design Engineer / Lattice Semiconductor
- **Agents on Rails: Deterministic AI for Chip Sign-Off**
  - Abhishikta Pandit / Principal Silicon EDA Infrastructure Engineer / Meta
- **ARTEMIS: A Multi-Agent AI System for Automated Root-Cause Analysis and Optimization of RTL Verification Workflows**
  - Nupur Bhonge / Solutions Architect / Amazon Web Services (AWS)
- **Formal Verification in the Age of Generative and Agentic AI**
  - Pallavi Atha / Principal Formal Verification Engineering Manager / Astera Labs
- **From Specification to Proof: AI Agent Automation of AEP and FPV Workflows**
  - Liya Liu / Senior Member of Technical Staff / AMD
- **Keep Your Data, Share the Model: Federated Learning for Privacy-Preserving Hardware Anomaly Detection Across Distributed Fleets**
  - Cynara Justine / Senior Software Engineer / Microsoft

11:10am **Breakout Sessions**
Design Technology

- **Achieving Glitch Power Visibility via Delay-Shifted Analysis for Actionable Power Optimization**
  - Neetu Mehta / Senior Silicon Design Engineer, India / AMD
- **Constraint-Driven Routing Closure for High-Frequency Mixed-Signal Designs**
  - Rajashri Lakhotia / Senior Staff Manager / Lattice Semiconductor
- **Executable Interrupt Specifications: Managing Control-Plane Complexity in Disaggregated Server SoCs**
  - Sindhu Amireddy / Principal SoC Architect / Microsoft

11:10am **Breakout Sessions**
Process Technology/Packaging/Assembly Technology

- **Failure Analysis Challenges of Advanced Packaging**
  - Susan Li / Technical Director of Failure Analysis / Marvell Technology
- **Mechanism-Driven Etch-Passivation Balance Engineering for LCDU Convergence Across Hybrid EUV Geometries**
  - Jasmine Chang / Dry Etch Process Engineer / Micron Technology
- **Split-Gate Inter-Poly Architecture for Electrical Gate Leakage and Mechanical Wafer Warpage Optimization in Trench FP-MOSFETs**
  - Irene Monica Lasar / Senior Technology Development Engineer / STMicroelectronics

11:10am

Breakout Sessions

System Technology

Making True Single-Stage EV Charging Practical: System-Level Redesign of a Dual-Active-Bridge On-Board Charger Enabled by Bidirectional GaN and Closed-Form Control

- Sara Bourouga / Technical Marketing & Application Support Engineer, EMEA Automotive organization / STMicroelectronics

Trivializing Chip Reset Timing: A Novel Technique for SoC Backend Closure

- Aradhana Kumari / Staff Engineer in Research & Innovation Team, Crolles, France / STMicroelectronics

Under the Hood of Hardware Systems for Accelerator-Aware Workloads: From Lab to Fleet

- Vidya Samyuktha / Principal Hardware Systems Engineer / Microsoft

11:10am

Breakout Sessions

Test/Validation

- **Capping with Confidence: A Test & Validation Framework for Power Savings and a Lower Data-Center Carbon Footprint**
  - Smitha Kashyap C / Principal Systems Engineer / Microsoft

- **On-Device Cryptographic Key Provisioning for PSA Level 3 MCU: Secure Firmware Synthesis and Attestation**
  - Lalitha Abraham / Principal Test Development Engineer / Synaptics

- **Scaling Electro-Optical Test and Reliability Validation of Silicon Photonics for AI and Hyperscale Data Centers**
  - Tala Sidawi / Senior Product Reliability Engineer / Ayar Labs

11:10am

Main Stage Panel

From First Role to Leadership: Real Stories of Award-Winning Women in Tech

Moderator:

• Isabelle Ferain / VP of Product Development Engineering / GlobalFoundries

Panelists:

• An Mei Chen / VP of Engineering and Site Lead for Vietnam R&D Center / Qualcomm
• Julie Sheridan Eng / EVP, Optical Components and Chief Technology Officer / Coherent
• Manisha Gambhir / Senior Director of Engineering, Analog Mixed Signal / Marvell Semiconductor
• Yanfang Le / Principal Network Architect / AMD

12:10pm **Lunch & Networking**

Welcoming Remarks by Rose Castanares

Sponsored by

tsmc®

2:00pm AyarLabs will announce winners of grantable wishes at the Wishing Wall area.

2:15pm **Welcome Back – Annelise McCarthy**

2:30pm **Awards Presentation**

- Dr. Lisa Su Woman of Innovation Award Presentation
- Rising Women in Semi Award Presentation

  Sponsored by SANDISK™
- Female Up & Comer Award Presentation

  Sponsored by AMD
- Designing the Difference Award Presentation

  Sponsored by Microsoft

3:00pm

Main Stage Panel

AI as a Catalyst: Building Resilient Teams and Inspiring Leadership

Moderator:

- Danielle Shown / VP of SCHIE Silicon, System, Platform AI and Scalability / Microsoft

Panelists:

- Alessandra Costa / SVP of Operations / Synopsys
- Alexis Björlin / Chief Strategy Officer / General Catalyst
- Qi Wang / Distinguished Engineer / Marvell Technology
- Viviana D'Alto / Artificial Intelligence SW & Tools Research Director (SRA/SCIS) / STMicroelectronics

3:50pm

Closing Remarks

4:10pm

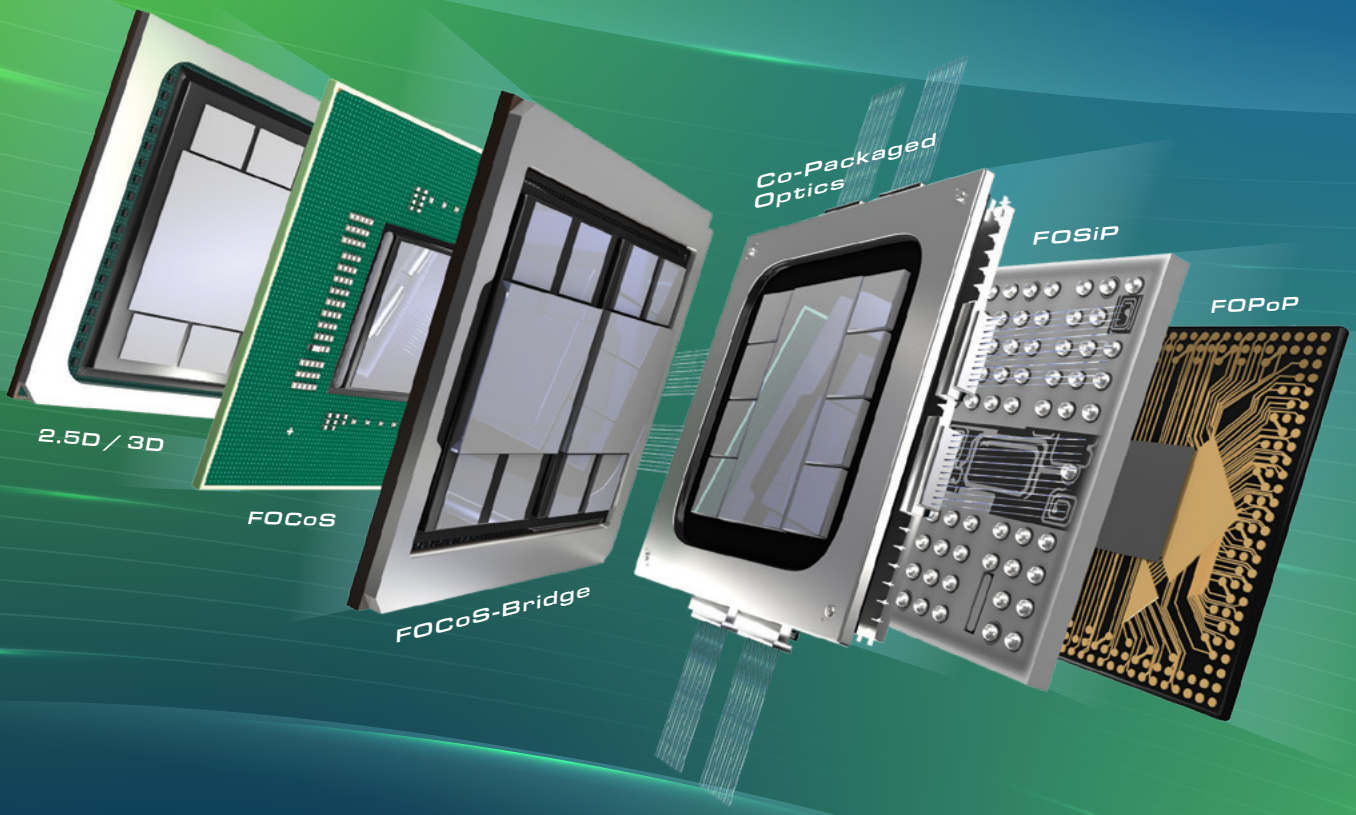
Networking & VIP Reception

Welcoming Remarks by Lalitha Immaneni

Sponsored by

intel foundry

ASE



ASE is proud to be a GSA WLI Advocate and a sponsor of WISH 2026. We're honored to support the Women's Leadership Initiative in building connections, advancing leadership and inspiring innovation across our industry.

From AI and HPC to data centers, robotics, automotive and IoT, ASE delivers sustainable advanced packaging that boosts performance and energy efficiency, from electrons to photons.

@aseglobal @aseglobal aseglobal.com